

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hou, et al. Attorney Docket: TSM03-0151
Filed: Herewith Examiner: TBD
Serial No.: TBD Art Unit: TBD
For: Method of Forming a Contact on a Silicon-on-Insulator Wafer

Mail Stop: Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed combined form PTO/SB/08A & 08B that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Pursuant to the waiver of the requirement under 37 CFR 1.98 (a)(2)(i), a copy of the U.S. patent reference is not enclosed.

If the Examiner should have any questions, please contact Applicants' attorney at the number listed below.

Respectfully submitted,



Barry W. Dove
Attorney for Applicants
Reg. No. 45,862

October 22, 2003
Date

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	TBD
				Filing Date	Herewith
				First Named Inventor	Hou, et al.
				Art Unit	TBD
Examiner Name	TBD				
Attorney Docket Number	TSM03-0151				
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US-6,194,739 B1	02-27-2001	Ivanov, et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	2	HU, C., "SOI and Nanoscale MOSFETs," Plenary Paper, Dept. of Electrical Engineering and Computer Sciences, University of California, Berkeley, CA.	
	3	YANG, F.L., et al., "25 nm CMOS Omega FETs," IEEE 2002.	
	4	COLINGE, J.P., et al., "SOI Devices for Sub-0.1µm Gate Lengths," Proc. 23 rd International Conference on Microelectronics (MIEL 2002), Vol. 1, NIS, Yugoslavia, May 12-15, 2002, pp. 109-113.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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